



Freeform Search

Database:	US Pre-Grant Publication Full-Text Database
	US Patents Full-Text Database
	US OCR Full-Text Database
	EPO Abstracts Database
	JPO Abstracts Database
	Derwent World Patents Index
	IBM Technical Disclosure Bulletins

Term:	117 and L18	
		

Display:	<input type="text" value="20"/>	Documents in Display Format:	<input type="text" value="-"/>	Starting with Number	<input type="text" value="1"/>
-----------------	---------------------------------	-------------------------------------	--------------------------------	-----------------------------	--------------------------------

Generate: ☐ Hit List ☒ Hit Count ☐ Side by Side ☐ Image

Search

Clear

Interrupt

Search History

DATE: Tuesday, November 06, 2007 [Purge Queries](#) [Printable Copy](#) [Create Case](#)

Set Name Query

side by side

Hit Count Set Name

result set

DB=PGPB,USPT; PLUR=YES; OP=OR

<u>L19</u>	117 and L18	38	<u>L19</u>
<u>L18</u>	711/\$.ccls.	39343	<u>L18</u>
<u>L17</u>	113 and L16	70	<u>L17</u>
<u>L16</u>	115 with l8	1095	<u>L16</u>
<u>L15</u>	address adj2 bit	39146	<u>L15</u>
<u>L14</u>	l8 and L13	899	<u>L14</u>
<u>L13</u>	l11 near4 l12	9033	<u>L13</u>
<u>L12</u>	operation or transaction	3361831	<u>L12</u>
<u>L11</u>	bridge	344197	<u>L11</u>
<u>L10</u>	l2 and l4	43	<u>L10</u>
<u>L9</u>	l6 and l8	10	<u>L9</u>
<u>L8</u>	address adj space	30274	<u>L8</u>
<u>L7</u>	l2 and L6	1	<u>L7</u>
<u>L6</u>	l3 and L5	10	<u>L6</u>
<u>L5</u>	BHATIA-SANDEEP\$.in.	60	<u>L5</u>
<u>L4</u>	l1 and L3	206	<u>L4</u>

<u>L3</u>	bus near3 bridge	17737	<u>L3</u>
<u>L2</u>	byte adj2 enabl\$	3644	<u>L2</u>
<u>L1</u>	((bridge and address adj2 space with address near3 bit) and (711/\$.ccls.))	328	<u>L1</u>

END OF SEARCH HISTORY


[Home](#) | [Login](#) | [Logout](#) | [Access Information](#) | [Alerts](#) | [Purchase History](#) | [Cart](#) | [Sitemap](#) | [Help](#)

Welcome United States Patent and Trademark Office

☐ Search Results

BROWSE

SEARCH

IEEE XPLORE GUIDE

SUPPORT

Results for "(bridge <and> (address space) <and> bus <and> (width <or> size)<in>met..."

Your search matched 24 of 1682970 documents.

☐ e-mail printer friendly

A maximum of 100 results are displayed, 25 to a page, sorted by Relevance in Descending order.

» Search Options

[View Session History](#)[New Search](#)

» Key

IEEE JNL IEEE Journal or Magazine

IET JNL IET Journal or Magazine

IEEE CNF IEEE Conference Proceeding

IET CNF IET Conference Proceeding

IEEE STD IEEE Standard

Modify Search

(bridge <and> (address space) <and> bus <and> (width <or> size)<in>metadata)

Search

☐ Check to search only within this results setDisplay Format: ☒ Citation ☐ Citation & Abstract

view selected items

[Select All](#) [Deselect All](#)

- ☐ 1. **Pinnacle: IBM MXT in a memory controller chip**
Tremaine, R.B.; Smith, T.B.; Wazlowski, M.; Har, D.; Kwok-Ken Mak; Arramreddy, S.;
[Micro, IEEE](#)
Volume 21, [Issue 2](#), March-April 2001 Page(s):56 - 68
Digital Object Identifier 10.1109/40.918003
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(192 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 2. **The Cambridge fast ring networking system**
Hopper, A.; Needham, R.M.;
[Computers, IEEE Transactions on](#)
Volume 37, [Issue 10](#), Oct. 1988 Page(s):1214 - 1223
Digital Object Identifier 10.1109/12.5983
[AbstractPlus](#) | Full Text: [PDF\(1008 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 3. **Bus architecture of a system on a chip with user-configurable system logic**
Winegarden, S.;
[Solid-State Circuits, IEEE Journal of](#)
Volume 35, [Issue 3](#), March 2000 Page(s):425 - 433
Digital Object Identifier 10.1109/4.826825
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(280 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 4. **Design and verification of a stack processor virtual component**
Stadler, M.; Thalmann, M.; Rower, T.; Kaeslin, H.; Felber, N.; Fichtner, W.;
[Micro, IEEE](#)
Volume 21, [Issue 2](#), March-April 2001 Page(s):69 - 80
Digital Object Identifier 10.1109/40.918004
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(300 KB\)](#) IEEE JNL
[Rights and Permissions](#)
- ☐ 5. **SH-5: the 64 bit superH architecture**
Biswas, P.; Hasegawa, A.; Mandaville, S.; Debbage, M.; Sturges, A.; Arakawa, F.; Saito, Y.;
Uchiyama, K.;
[Micro, IEEE](#)
Volume 20, [Issue 4](#), July-Aug. 2000 Page(s):28 - 39
Digital Object Identifier 10.1109/40.865864

[AbstractPlus](#) | [References](#) | Full Text: [PDF\(152 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **6. Design approaches and performance criteria for integrated voice/data switching**
Ross, M.J.; Tabbot, A.C.; Waite, J.A.;
[Proceedings of the IEEE](#)
Volume 65, [Issue 9](#), Sept. 1977 Page(s):1283 - 1295
[AbstractPlus](#) | Full Text: [PDF\(1386 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **7. AMULET2e: an asynchronous embedded controller**
Furber, S.B.; Garside, J.D.; Riocreux, P.; Temple, S.; Day, P.; Jianwei Liu; Paver, N.C.;
[Proceedings of the IEEE](#)
Volume 87, [Issue 2](#), Feb. 1999 Page(s):243 - 256
Digital Object Identifier 10.1109/5.740018
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(420 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **8. A 7.5 kW ultrasonic inverter motor drive employing MOS-controlled thyristors**
Erdman, W.L.; Hudson, R.; Yang, J.; Hoft, R.G.;
[Industry Applications, IEEE Transactions on](#)
Volume 26, [Issue 4](#), July-Aug. 1990 Page(s):756 - 768
Digital Object Identifier 10.1109/28.56003
[AbstractPlus](#) | Full Text: [PDF\(736 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **9. Architecture, Design, and Development of the System 75 Office Communications System**
Feiner, A.;
[Selected Areas in Communications, IEEE Journal on](#)
Volume 3, [Issue 4](#), Jul 1985 Page(s):522 - 530
[AbstractPlus](#) | Full Text: [PDF\(1024 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **10. Hector: a hierarchically structured shared-memory multiprocessor**
Vranesic, Z.G.; Stumm, M.; Lewis, D.M.; White, R.;
[Computer](#)
Volume 24, [Issue 1](#), Jan. 1991 Page(s):72 - 79
Digital Object Identifier 10.1109/2.67196
[AbstractPlus](#) | Full Text: [PDF\(696 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **11. A 200-MHz 64-b dual-issue CMOS microprocessor**
Dobberpuhl, D.W.; Witek, R.T.; Allmon, R.; Anglin, R.; Bertucci, D.; Britton, S.; Chao, L.;
Conrad, R.A.; Dever, D.E.; Gieseke, B.; Hassoun, S.M.N.; Hoepfner, G.W.; Kuchler, K.; Ladd,
M.; Leary, B.M.; Madden, L.; McLellan, E.J.; Meyer, D.R.; Montanaro, J.; Priore, D.A.;
Rajagopalan, V.; Samudrala, S.; Santhanam, S.;
[Solid-State Circuits, IEEE Journal of](#)
Volume 27, [Issue 11](#), Nov. 1992 Page(s):1555 - 1567
Digital Object Identifier 10.1109/4.165336
[AbstractPlus](#) | Full Text: [PDF\(1220 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **12. SWAN: a mobile multimedia wireless network**
Agrawal, P.; Hyden, E.; Krzyzanowski, P.; Mishra, P.; Srivastava, M.B.; Trotter, J.A.;
[Personal Communications, IEEE \[see also IEEE Wireless Communications\]](#)
Volume 3, [Issue 2](#), April 1996 Page(s):18 - 33
Digital Object Identifier 10.1109/98.490750

[AbstractPlus](#) | Full Text: [PDF\(6072 KB\)](#) IEEE JNL

[Rights and Permissions](#)

- ☐ **13. IP lookups using multiway and multicolumn search**
Lampson, B.; Srinivasan, V.; Varghese, G.;
[Networking, IEEE/ACM Transactions on](#)
Volume 7, Issue 3, June 1999 Page(s):324 - 334
Digital Object Identifier 10.1109/90.779199
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(176 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **14. Firewire in modern integrated military avionics**
Baltazar, G.; Chapelle, G.P.;
[Aerospace and Electronic Systems Magazine, IEEE](#)
Volume 16, Issue 11, Nov. 2001 Page(s):12 - 16
Digital Object Identifier 10.1109/62.965006
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(416 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **15. Index rendering: hardware-efficient architecture for 3-D graphics in multimedia system**
Bor-Sung Liang; Yuan-Chung Lee; Wen-Chang Yeh; Chein-Wei Jen;
[Multimedia, IEEE Transactions on](#)
Volume 4, Issue 3, Sept. 2002 Page(s):343 - 360
Digital Object Identifier 10.1109/TMM.2002.802008
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(642 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **16. VI-attached database storage**
Zhou, Y.; Bilas, A.; Jagannathan, S.; Xinidis, D.; Dubnicki, C.; Li, K.;
[Parallel and Distributed Systems, IEEE Transactions on](#)
Volume 16, Issue 1, Jan 2005 Page(s):35 - 50
Digital Object Identifier 10.1109/TPDS.2005.13
[AbstractPlus](#) | Full Text: [PDF\(1144 KB\)](#) IEEE JNL
[Rights and Permissions](#)

- ☐ **17. The Information Utility: a project retrospective**
Slonim, J.; Bauer, M.;
[Software Engineering Journal](#)
Volume 5, Issue 4, July 1990 Page(s):223 - 236
[AbstractPlus](#) | Full Text: [PDF\(1156 KB\)](#) IET JNL

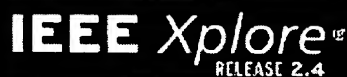
- ☐ **18. A New Type of Embedded Autolevelling Control System based on ARM Microcontroller for Carding Machine**
Xu, R.Z.; Guo, W.; Li, C.X.;
[Industrial Electronics and Applications, 2006 1ST IEEE Conference on](#)
24-26 May 2006 Page(s):1 - 6
Digital Object Identifier 10.1109/ICIEA.2006.257151
[AbstractPlus](#) | Full Text: [PDF\(340 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **19. A Nanoscale Memory Interface Scheme based on Hierarchical Memory Mapping**
Venkatasubramanian, G.; Figueiredo, R.J.;
[Nanotechnology, 2006. IEEE-NANO 2006. Sixth IEEE Conference on](#)
Volume 1, 17-20 June 2006 Page(s):298 - 301
[AbstractPlus](#) | Full Text: [PDF\(224 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **20. Completely digital control unit of a high dynamic synchronous servodrive with minimal hardware investment using a fast microcontroller**

Naunin, D.; Karaali, C.;
Power Electronics Specialists Conference, 1992. PESC '92 Record., 23rd Annual IEEE
29 June-3 July 1992 Page(s):686 - 690 vol.1
Digital Object Identifier 10.1109/PESC.1992.254816
[AbstractPlus](#) | Full Text: [PDF\(292 KB\)](#) IEEE CNF
[Rights and Permissions](#)

- ☐ **21. Fast paths in concurrent programs**
Xu, W.; Sanjeev Kumar; Li, K.;
Parallel Architecture and Compilation Techniques, 2004. PACT 2004. Proceedings. 13th International Conference on
29 Sept.-3 Oct. 2004 Page(s):189 - 200
Digital Object Identifier 10.1109/PACT.2004.1342553
[AbstractPlus](#) | Full Text: [PDF\(463 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **22. Bridging the gap between future software and hardware engineers: a case study using the Nios softcore processor**
Kairus, J.; Forsten, J.; Tommiska, M.; Skytta, J.;
Frontiers in Education, 2003. FIE 2003. 33rd Annual
Volume 2, 5-8 Nov. 2003 Page(s):F2F - 1-5 Vol.2
Digital Object Identifier 10.1109/FIE.2003.1264713
[AbstractPlus](#) | Full Text: [PDF\(435 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **23. A comparative throughput analysis of Scalable Coherent Interface and Myrinet**
Millich, S.; George, A.; Oral, S.;
Local Computer Networks, 2002. Proceedings. LCN 2002. 27th Annual IEEE Conference on
6-8 Nov. 2002 Page(s):691 - 700
[AbstractPlus](#) | Full Text: [PDF\(552 KB\)](#) IEEE CNF
[Rights and Permissions](#)
- ☐ **24. Scalability evaluation of multi-protocol over ATM (MPOA)**
Widjaja, I.; Wang, H.; Wright, S.; Chatterjee, A.;
INFOCOM '99. Eighteenth Annual Joint Conference of the IEEE Computer and Communications Societies. Proceedings. IEEE
Volume 3, 21-25 March 1999 Page(s):1505 - 1512 vol.3
Digital Object Identifier 10.1109/INFCOM.1999.752172
[AbstractPlus](#) | Full Text: [PDF\(668 KB\)](#) IEEE CNF
[Rights and Permissions](#)



Edit an existing query or compose a new query in the Search Query Display.

Tue, 6 Nov 2007, 5:05:32 PM EST

Search Query Display

Select a search number (#) to:

- Add a query to the Search Query Display
- Combine search queries using AND, OR, or NOT
- Delete a search
- Run a search

Recent Search Queries

		Results
#1	(byte enable) <in> metadata	0
#2	(byte enable) <in> metadata	0
#3	((bus conversion bridge)<in>metadata)	0
#4	(adapt* <and> bus <and> size<IN>metadata)	1240
#5	(bridge <and> (address space) <and> bus<IN>metadata)	79
#6	(bridge <and> (address space) <and> bus <and> (width <or> size)<in>metadata)	24